

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,882,055 B1  
DATED : April 19, 2005  
INVENTOR(S) : Steven Teig et al.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 51,

Line 1, "An integrated-circuit ("IC") layout comprising;" should read -- An integrated-circuit ("IC") layout comprising: --.

Signed and Sealed this

Thirty-first Day of January, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*